

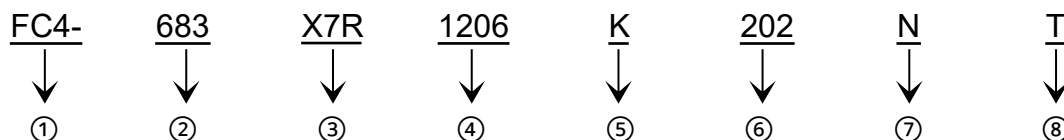
“Military Screening” Grade Two Multi Layer Ceramic Capacitors



1. Features

- Rectangular, serialized size specifications, suitable for surface mount of hybrid integrated circuits or printed circuits;
- Lead type: radial lead, suitable for plug-in and wave soldering;
- There are a variety of end electrode lead-out materials for Ag (Cu)-Ni-Sn, which are especially suitable for the strict requirements of surface assembly technology for weldability and welding heat resistance.;
- Low inductance, low loss, good frequency characteristics, high reliability;
- National military standard production line production;
- Widely used in military communications, radar, artillery fuzes, ships, ground electronic equipment for aviation, aerospace, and weapon systems, and high-end civilian equipment.
- It is suitable for resonance circuits, coupling circuits and circuits that require low loss, high capacitance, stability and high insulation resistance in various types of military electronic equipment.

Purtnumber structure



Description:

① Product series.

FC4: Class I dielectric material type (COG)

FX4: Class II dielectric material type (Other TCC).

② Rated capacity code.

Capacity code	Actual value (pF)	Remark
0R5	0.5pF	The first two digits are valid figures, and the last digit is a power of 10.
1R0	1.0pF	
472	47×10^2	
104	10×10^4	

③ Dielectric material.

Class I (FC4 series): C0G.

Class II (FX4 series): X7R, X5R, Y5V, Z5U, X6S, X7T, X7S and other materials.

④ Size.

Dimensions	01005	0201	0402	0603	0805	1206	1210	1808	1812	2211
Length×Width (inch)	0.01×0.005	0.02×0.01	0.04×0.02	0.06×0.03	0.08×0.05	0.12×0.06	0.12×0.10	0.18×0.08	0.18×0.12	0.22×0.11
Length×Width (mm)	0.40×0.20	0.60×0.30	1.00×0.50	1.52×0.76	2.00×1.25	3.20×1.60	3.20×2.50	4.50×2.00	4.50×3.20	5.70×2.80

⑤ Capacity tolerance level

code	A	B	C	D	f	G	J	K	M	L	Z
tolerance	±0.05pF	±0.10pF	±0.25pF	±0.50pF	±1%	±2%	±5%	±10%	±20%	±30%	-20~+80%
Remark	A, B, C, D tolerance level is generally used for ≤10pF capacity										

⑥ Rated voltage

Voltage code	Actual value (V)	Remark
6R3	6.3	The first two digits are valid figures, and the last digit is 10 index of R as decimal point
500	50×10 ⁰	
201	20×10 ¹	
102	10×10 ²	
202	20×10 ²	

⑦ Terminal plating

Terminal plating type	Plating Code
Three-layer plating Ag(Cu)/Ni/Sn	N
Silver	S
Gold	A
Tin/lead alloy containing at least 3% lead	Z

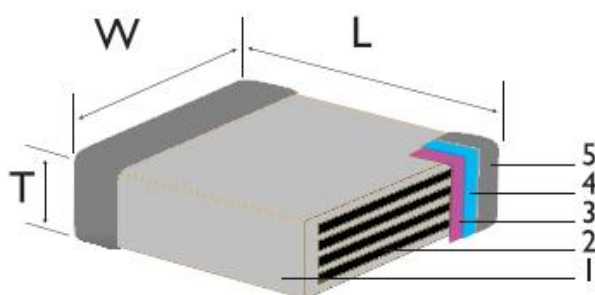
⑧ Packaging type

Packaging type	Packaging Code
Bulk	B
Taping	T

Size specifications



Size code		Size (mm)		
inch	mm	L	W	Tmax
01005	0402	0.40 ± 0.02	0.20 ± 0.02	0.22
0201	0603	0.60 ± 0.05	0.30 ± 0.05	0.35
0402	1005	1.00 ± 0.20	0.50 ± 0.20	0.70
0603	1608	1.52 ± 0.25	0.76 ± 0.25	1.01
0805	2012	2.00 ± 0.25	1.25 ± 0.25	1.45
1206	3216	3.20 ± 0.30	1.60 ± 0.30	1.80
1210	3225	3.20 ± 0.30	2.50 ± 0.30	2.80
1808	4520	4.50 ± 0.40	2.00 ± 0.20	2.80
1812	4532	4.50 ± 0.40	3.20 ± 0.30	3.10
2211	5728	5.70 ± 0.50	2.80 ± 0.30	3.10
2220	5750	5.70 ± 0.50	5.00 ± 0.50	4.90
2225	5763	5.70 ± 0.50	6.30 ± 0.50	6.20
3035	7689	7.60 ± 0.50	9.00 ± 0.50	6.20
3838	9696	9.80 ± 0.50	9.20 ± 0.50	4.50



Number	Item
1	ceramic dielectric
2	Internal electrode
3	External electrode
4	Nickel layer
5	Tin layer

Conventional voltage product capacity range

Specification	0402					0603					0805					1206				
(V)	6.3	10	16	25	50	6.3	10	16	25	50	6.3	10	16	25	50	6.3	10	16	25	50
C _R																				
100pF																				
220pF																				
330pF																				
470pF																				
680pF																				
1 nF																				
2.2nF																				
4.7nF																				
5.6nF																				
6.8nF																				
10nF																				
15nF																				
22nF																				
27nF																				
33nF																				
39nF																				
47nF																				
56nF																				
68nF																				
100nF																				
150nF																				
220nF																				
270nF																				
330nF																				
470nF																				
820nF																				
1.0uF																				
2.2uF																				
4.7uF																				
6.8uF																				
10uF																				
22uF																				
27uF																				
33uF																				
47uF																				
100uF																				

 X7R
  X5R

Medium and high voltage product capacity range

Specification	0	0805			1206					1210				
(V) C _R	1	100	200	250	100	250	500	1000	2000	100	250	500	1000	2000
100pF														
150pF														
330pF														
470pF														
680pF														
1000pF														
1. 2nF														
1. 5nF														
2. 2nF														
3. 3nF														
4. 7nF														
6. 8nF														
10nF														
12nF														
15nF														
22nF														
27nF														
33nF														
39nF														
47nF														
56nF														
68nF														
100nF														
120nF														
150nF														
220nF														
270nF														
330nF														
470nF														
560nF														
1. 0uF														
2. 2uF														

Specification	1808						1812						
(V) C _R	100	250	500	1000	2000	3000	100	250	500	1000	2000	3000	4000
100pF													
150pF													
330pF													
470pF													
680pF													
1000pF													
1. 5nF													
2. 2nF													
2. 7nF													
3. 3nF													
4. 7nF													
6. 8nF													
10nF													
12nF													
15nF													
22nF													
27nF													
33nF													
39nF													
47nF													
56nF													
68nF													
100nF													
120nF													
150nF													
220nF													
270nF													
330nF													
470nF													
2. 2uF													

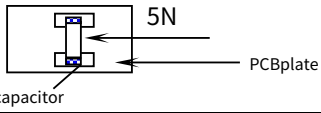
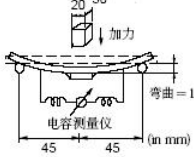
Specification	2225						
(V) CR	100	250	500	1000	2000	3000	4000
100pF							
150pF							
180pF							
220pF							
330pF							
470pF							
680pF							
1000pF							
1. 5nF							
2. 2nF							
3. 3nF							
4. 7nF							
6. 8nF							
10nF							
12nF							
15nF							
22nF							
27nF							
33nF							
39nF							
47nF							
56nF							
68nF							
100nF							
120nF							
150nF							
220nF							
270nF							
330nF							
470nF							
820nF							
1uF							
2. 2uF							

Note: When customers have special requirements, please specify when ordering.

Product technical specifications

Project	Technical Specifications/Test Methods							Remark		
range of working temperature	Class I	C0G:-55~+125°C								
	Class II	first digit	second digit		third digit		Foe example "X7R":			
		Lower limit temperature	upper limit category temperature		relatively to +25°C Capacity can change in full operating temperature range		The first letter "X" Indicates that its lowest operating temperature is -55°C.			
		X:-55°C	4:+65°C	7:+125°C	P:±10%	T:+22%/-33%	The second digit "7" Indicates that its highest operating temperature is +125°C.			
		Y:-30°C	5:+85°C	8:+150°C	R:±15%	U:+22%/-56%	The third letter "R" Indicates that value of Capacity can change not more than ±15% when temperature is change during full operating temperature range.			
		Z:+10°C	6:+105°C		S:±22%	V:+22%/-82%				
Exterior		Visual inspection, product appearance has no obvious defects								
Electrostatic capacity (C)	Class I	Within specification error		Nominal capacity ≤1000pF >1000pF	Test frequency 1MHz±10% 1KHz±10%	Test voltage 1.0±0.2Vrms 1.0±0.2Vrms	environment temperature: +25±2°C Humidity: <75%			
	Class II	Within specification error		≤10μF >10μF Z5U	1KHz±10% 120Hz/100Hz 1.0±0.1KHz	1.0±0.2Vrms 0.5±0.2Vrms 0.5±0.05Vrms				
loss tangent (DF)	Class I	Cap≤50pF, DF≤1.5×(150/CR+7)×10 ⁻⁴ ; Cap≥50pF, DF≤0.15%								
	Class II	Rated voltage	≥100V	50V	25V	16V	10V	6.3V		
		Except for Y5V and Z5U, other temperature characteristics, such as X7R/X8S	≤2.5%	≤3.5% (C<1μF)		≤5.0% (C<1μF)		≤7.5% (C≤3.3μF)		≤7.5% (C<3.3μF)
				≤7.5% (1μF≤C<10μF)		≤7.5% (1μF≤C<10μF)		≤10.0% (C>3.3μF)		≤10.0% (C>3.3μF)
				≤10.0% (C≥10μF)		≤10.0% (C≥10μF)				
		Y5V Z5U	≥25V				3.6V≤U _R <25V			
≤7.0% (C≤1μF)				≤12.5%						
Insulation resistance (IR)	Class I	C≤10nF,Ri≥10000MΩ C>10nF,Ri*C _R ≥100S		Rated voltage Ur<1000V	Test voltage Ur	testing time 60±5sec	Charge and discharge current <50mA	environment temperature:25±2°C Humidity:<75%		
	Class II	C≤25nF,Ri≥4000MΩ C>25nF,Ri*CR>100S		Ur≥1000V	1000V	60±5sec	<50mA			
Dielectric strength (DWV)	Class I	There should be no breakdown or damage to the medium (Extremely designed products such as 1206X7R102K282NB,2211C0G150Z722 N.B.Its "test voltage" is1.1Ur)		Rated voltage Ur<200V	Test voltage 2.5Ur	Slow rise time 0sec	testing time 1~5sec	environment temperature:25±2°C Humidity:<75%		
	Class II			200V≤Ur≤1000V Ur>1000V	1.5Ur 1.2Ur	1~5sec 1~5sec	1~5sec 1~5sec	Charge and discharge current: ≤50mA		
Capacity temperature coefficient or temperature characteristics	Class I	C0G: 0±30ppm/°C		preprocessing(IIkind):150+0/-10°C,1hrs,place24±2hrsAfter initial measurement, according to the following temperature sequence, the temperature is stable30minAfter measurement (△CbyT3shall prevail)						
	Class II	See the third digit in "Operating Temperature Range" number", such as "X7R" , the third digit is "R",Right now"±15%" .		step T1 T2 T3 T4 T1	Temperature (°C) 20±2 Lower limit category temperature (e.g.X7R-55±3) 20±2 Upper category temperature (e.g.X7R 125±2) 20±2					
Solderability	Class I Class II	No visible damage on appearance, tin rate≥95%		Soak the capacitor in ethanol and rosin (account25%weight) solution, take it out in 80~120 Preheat at °C temperature10~30sec, and then immerse it in the solder solution. Immersion tin temperature:235±5°C; Tin immersion speed: 25±0.25mm/s Tin immersion time:2±0.5sec						

Product technical specifications

Resistant to welding heat	Exterior	No visible damage, tin coating rate $\geq 95\%$	Soak the capacitor in ethanol and rosin (account25%weight) solution, take it out in 100~200Preheat at $^{\circ}\text{C}$ temperature $10\pm 2\text{min}$, and then immerse it in the solder solution.															
	$\Delta C/C$	Ikind: $\pm 0.5\%$ or $\pm 0.5\text{pF}$, whichever is greater	Immersion tin temperature: $260\pm 5^{\circ}\text{C}$; immersion tin speed: $25\pm 0.25\text{mm/s}$ Tin dipping time: $10\pm 1\text{sec}$															
		IIkind:X7R X6S X5R X7S: $\pm 10\%$ X7T: $\pm 15\%$ Y5V Z5U: $\pm 20\%$																
	DF	Same as initial standard	After taking it out, clean it with solvent, and100bserve under a microscope at magnification.															
IR	Same as initial standard	Place indoors after testing $24\pm 2\text{hrs}$ Measure again later.																
terminal electrode Adhesion strength	The terminal electrode does not peel off and there is no visible damage on the appearance.		Apply thrust:5N time: $10\pm 1\text{sec}$ speed: $1\pm 0.5\text{mm/sec}$ 															
Bending strength	Exterior	no visible damage	Test substrate:PCB Bend depth:1mm															
	$\Delta C/C$	Ikind: $\pm 5\%$	Pressure speed: $1\pm 0.5\text{mm/sec}$. Measurements should be made in the bent state. 															
		IIkind: $\pm 10\%$																
temperature cycle	Exterior	no visible damage	preprocessing(Ikind): $150+0/-10^{\circ}\text{C}$,1hrs,place $24\pm 2\text{hrs}$ Number of post-initial measurement cycles:5times, one cycle is divided into the following4step:															
	$\Delta C/C$	Ikind: $\pm 1\%$ or $\pm 1\text{pF}$ Whichever of the two is greater	<table><thead><tr><th>stage</th><th>Temperature$^{\circ}\text{C}$</th><th>time (minutes)</th></tr></thead><tbody><tr><td>No.1step</td><td>Limit category temperature</td><td>30</td></tr><tr><td>No.2step</td><td>20 ± 2</td><td>3</td></tr><tr><td>No.3step</td><td>Limit category temperature</td><td>30</td></tr><tr><td>No.4step</td><td>20 ± 2</td><td>3</td></tr></tbody></table>	stage	Temperature $^{\circ}\text{C}$	time (minutes)	No.1step	Limit category temperature	30	No.2step	20 ± 2	3	No.3step	Limit category temperature	30	No.4step	20 ± 2	3
		stage	Temperature $^{\circ}\text{C}$	time (minutes)														
		No.1step	Limit category temperature	30														
	No.2step	20 ± 2	3															
No.3step	Limit category temperature	30																
No.4step	20 ± 2	3																
IIkind:X7R X6S X5R X7S $\pm 10\%$ X7T $\pm 15\%$ Z5U Y5V $\pm 20\%$																		
DF	Same as initial standard																	
IR	Same as initial standard	Place indoors after testing $24\pm 2\text{hrs}$ Measure again later.																
Steady state moisture test	Exterior	no visible damage	temperature: $40\pm 2^{\circ}\text{C}$ humidity: $90\sim 95\%\text{RH}$ time: $500+24/-0\text{Hour}$ Storage conditions: Place at room temperature time:twenty fourHour(Ikind);48Hour(IIkind)															
	$\Delta C/C$	Ikind: $\pm 2\%$ or $\pm 1\text{pF}$ Whichever of the two is greater																
		IIkind:X7R X6S X5R X7S $\pm 10\%$ X7T $\pm 15\%$ Z5U Y5V $\pm 30\%$																
		DF		≤ 2 times the initial standard														
	IR	Ikind: $R_i\geq 2500\text{M}\Omega$ or $R_i\cdot C_R> 25\text{S}$ Whichever of the two is smaller IIkind: $R_i\geq 1000\text{M}\Omega$ or $R_i\cdot C_R> 25\text{S}$ Whichever of the two is smaller																
Life test	Exterior	no visible damage	Rated voltage $U_r<500\text{V}$ $500\text{V}\leq U_r\leq 1000\text{V}$ $U_r>1000\text{V}$ Apply voltage $2U_r$ $1.5U_r$ $1.2U_r$ Charge and discharge current: $\leq 50\text{mA}$ Temperature: upper limit category temperature time: $96\pm 4\text{Hour}$ Storage conditions: room temperature Placement time:twenty fourHour(Ikind);48Hour(IIkind)															
	$\Delta C/C$	Ikind: $\pm 2\%$ or $\pm 1\text{pF}$ Whichever of the two is greater																
		IIkind:X7R X7T X6S X5R X7S $\pm 20\%$ Z5U Y5V $\pm 30\%$																
		DF		≤ 2 times the initial standard														
	IR	Ikind: $R_i\geq 4000\text{M}\Omega$ or $R_i\cdot C_R> 40\text{S}$ Whichever of the two is smaller IIkind: $R_i\geq 2000\text{M}\Omega$ or $R_i\cdot C_R> 50\text{S}$ Whichever of the two is smaller																

Precautions for use

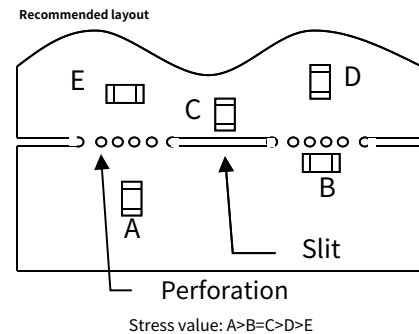
1. Precautions before use:

In the harsh working environment or external mechanical overpressure that exceeds the usage conditions stated in the relevant instructions of this acknowledgment, the MLCC chip may be damaged. Therefore, when using it, first consider applying according to the relevant instructions of this acknowledgment.

2. PC board design

2.1 The amount of solder used will affect the chip's ability to withstand mechanical stress, which may cause the MLCC to break or crack. Therefore, when designing a substrate, careful consideration must be given to the size and configuration of the pads, which determine the amount of solder that makes up the substrate.

2.2 When designing the location of the pads and SMD MLCC, consideration should be given to minimizing stress, and the MLCC should be installed at the least affected location on the PC board.



3. Issues that should be considered during automatic installation

If the pickup tube is lowered beyond the lowest limit, excessive pressure will be exerted on the MLCC, causing the MLCC to rupture. When lowering the suction tube, pay attention to the following points:

3.1 After correcting the deviation of the PC board, the low limit of the pick-up tube should be adjusted to the horizontal position of the surface of the PC board.

3.2 The suction pressure should be adjusted to between 1 and 3N.

3.3 In order to reduce the deformation of the PC board caused by the impact force of the suction pipe, the support nails should be placed under the PC board.

4. Welding

4.1 MLCC is a combination of ceramic and metal.

As a ceramic body, especially a large-sized ceramic body, its thermoplasticity is poor and its response to heat is relatively slow. Ceramic bodies are prone to cracking when subjected to rapid cooling and heating. It is recommended to conduct continuous preheating for more than 1 minute before welding.

4.2 The interior of MLCC is a metal electrode.

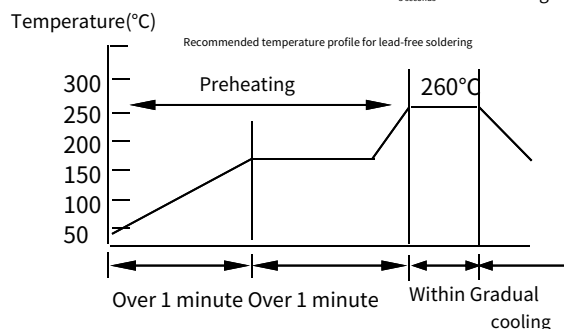
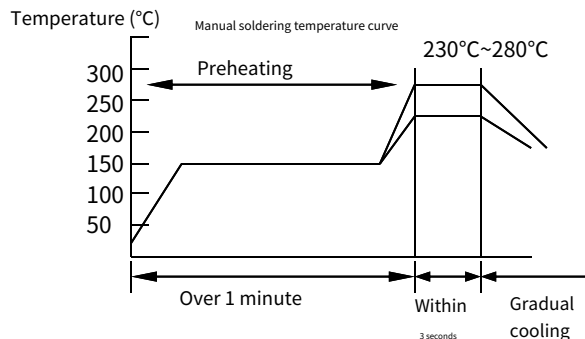
It is very thermoplastic and responds quickly to heat. Thus, in When heated, metal parts and ceramic parts are bound to Expansion is inconsistent to a certain extent, resulting in internal

Internal stress can easily cause the porcelain body to crack. It is recommended that before welding Perform continuous preheating for more than 1 minute.

4.3 When manual soldering, use the straight tip of the soldering iron with a constant temperature.

The maximum diameter is 1.0mm and the maximum power is 25 watts; the soldering iron cannot directly touch the MLCC components.

4.4 It is recommended to avoid using wave soldering for specifications above 1206.



5. Cleaning

5.1 The temperature difference between the components and the cleaning process cannot be greater than 100°C.

5.2 In the case of ultrasonic cleaning, too much output power will cause the PC board to undergo excessive vibration, which will cause the MLCC or solder joints to crack, or reduce the strength of the terminal electrodes. Therefore, special attention should be paid to the following points:

Ultrasonic output: less than 20W/L; ultrasonic frequency: less than 40KHz; ultrasonic cleaning time: 5 minutes or less

6. Cutting PCB board

6.1 After installing the MLCC and other components, when dividing the PC board, be careful not to apply any force on the board. MLCC cannot be allowed to withstand excessive mechanical shock.

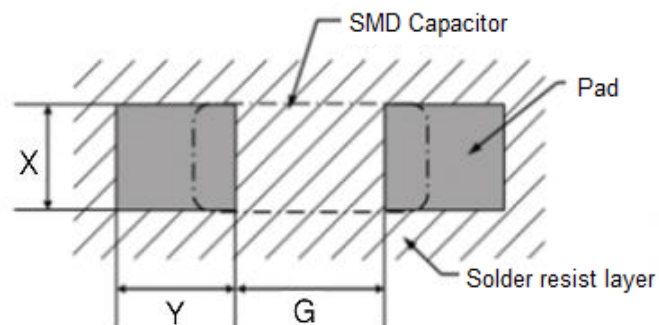
6.2 The segmentation of boards cannot be done manually and appropriate equipment should be used.

7. Storage method

In order to maintain the solderability of the terminal electrodes and ensure that the packaging materials are in good condition, the recommended storage conditions are as follows: Storage temperature: 5-40°C; Storage relative humidity: 20-70%RH

Even when stored under ideal storage conditions, MLCC tip solderability will degrade over time, so MLCCs should be used within 6 months from the date of shipment.

Pad recommendation



Reflow welding pad size						mm
Dimensions	G		X		Y	
	min	max	min	max	min	max
01005	0.16	0.20	0.20	0.23	0.12	0.18
0201	0.20	0.30	0.25	0.40	0.20	0.30
0402	0.40	0.60	0.40	0.60	0.30	0.50
0603	0.50	1.00	0.60	1.00	0.60	0.80
0805	0.60	1.20	0.90	1.60	0.70	1.30
1206	1.80	2.50	1.20	2.00	1.00	1.60
1210	1.80	2.50	1.80	3.20	1.00	1.60
1812	2.50	3.70	2.30	3.50	1.20	1.90
2220	3.20	4.70	3.50	5.00	1.20	2.30
2225	3.20	4.70	3.50	6.80	1.20	2.30
3035	3.30	6.00	8.00	9.50	1.60	3.00
4045	7.00	8.60	10.80	12.00	1.60	3.00
5868	11.50	13.20	16.50	18.20	1.60	3.00